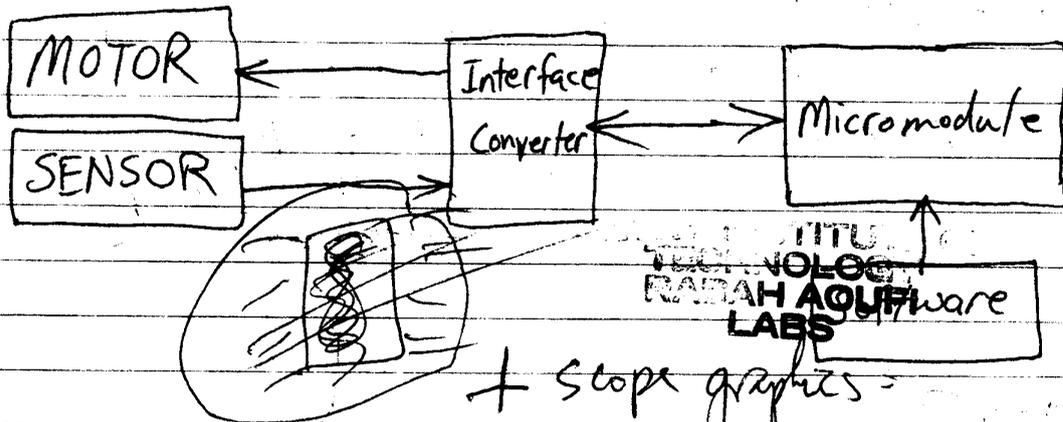


Alan Lilly

Light Sensitive Tracking Device

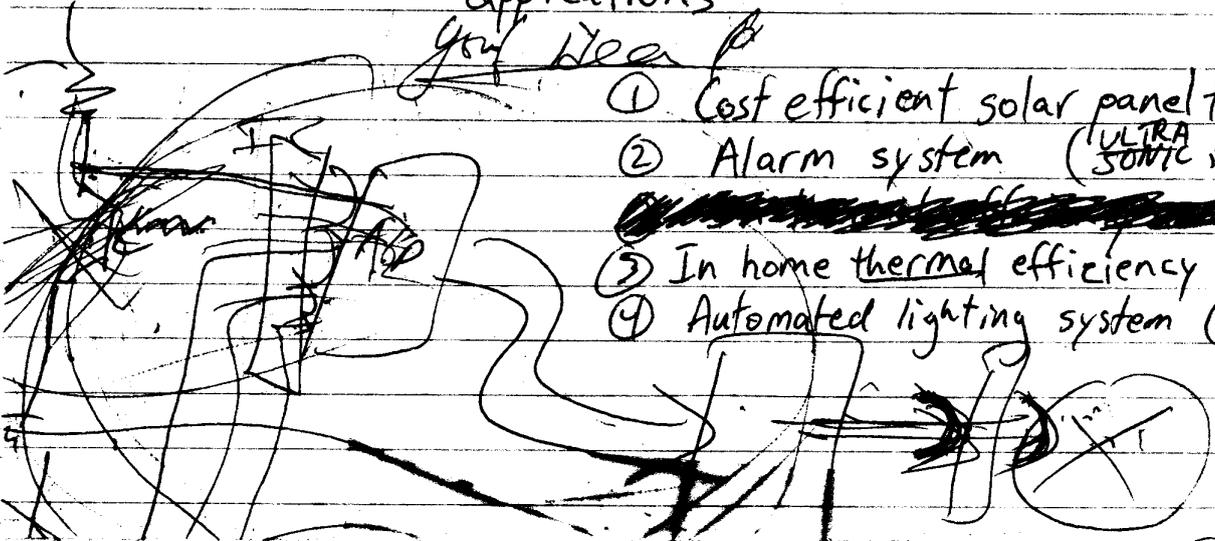
Objective: To construct and make operational a device that can physically track a light source of relatively high intensity (graphical printout of relative intensities)



+ Scope graphics =

Application: Depending on the type of sensor and the software used, this system can have several applications

- ① Cost efficient solar panel tracking
- ② Alarm system (ULTRA SONIC, LIGHT)
- ~~③ In home thermal efficiency analysis~~
- ④ Automated lighting system (SONIC)



Alan Lilly

4-1-87

Determining required parts:

- 1 Stepper motor (accurate to within 5°) ✓
- 2 photosensitive transistors ✓
- 1 parabolic reflector ✓
- 1 spool wire wrap ✓

Note: construction of base (wood, metal ^{sheet})

(DALLAS ELECTRONIC SURPLUS)

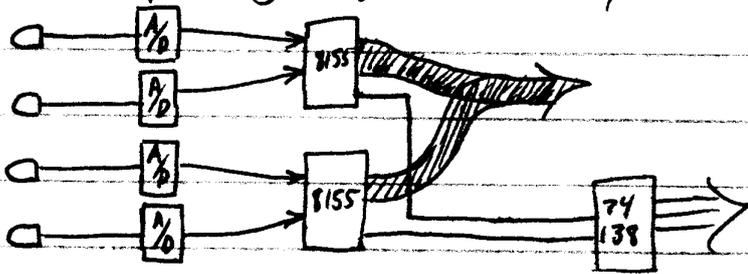
Incorporate eeprom expansion for software

Consider dual motor control
Consider ultrasonic sensor

4/1/87

3-24-87

Multiplexing might be a cheaper answer



4-8-87

12 v 7.5° stepper motor sequence

white leads - grounded

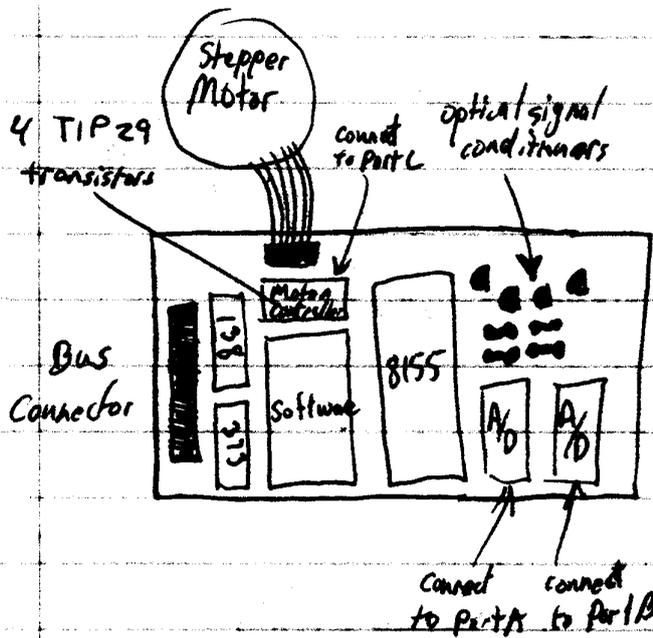
Brown

Yellow

Red

Blue

Counter
Clockwise



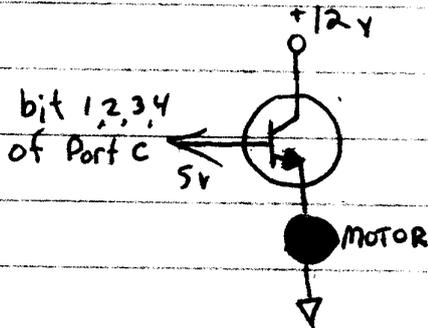
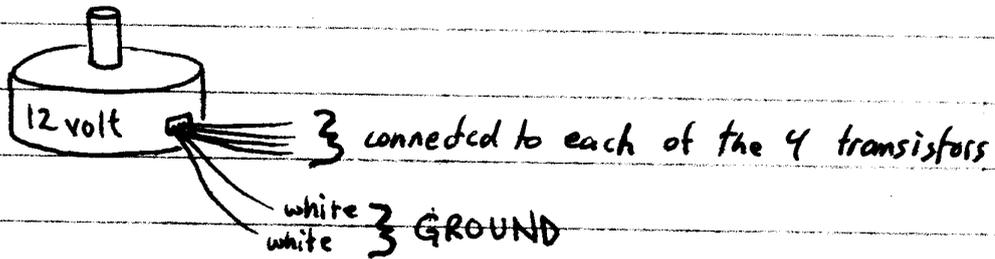
- 2 3964 (ECG 123AP) ✓
- 2 2907 (ECG 159)
- 2 3906 (ECG 159) ✓

DEV: ... OF
RND LABS

4-15-87

Alan Lilly

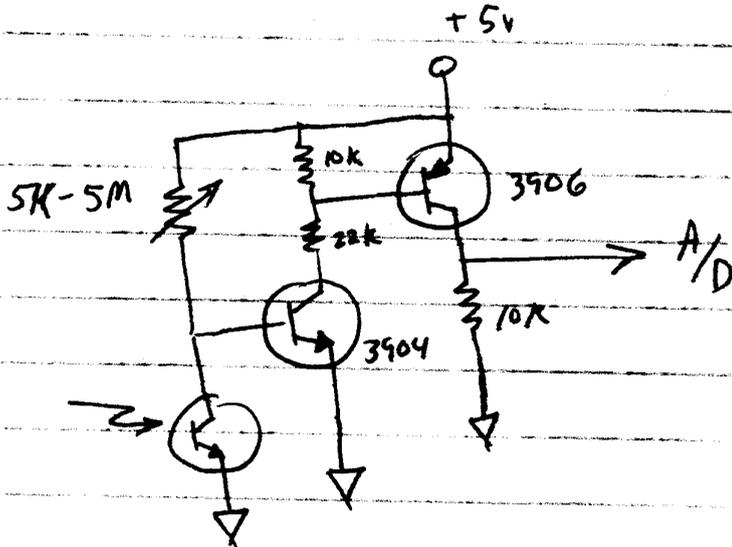
Test TIP29 transistors for
stepper motor controllers



4-15-87

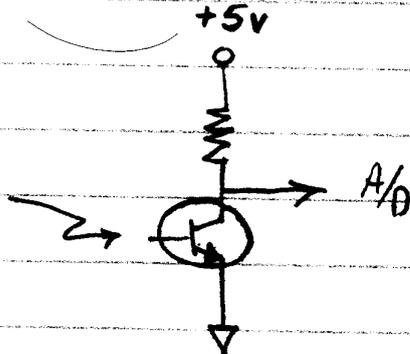
Alan Zilly

Highly sensitive circuit (photo sensor)



OR

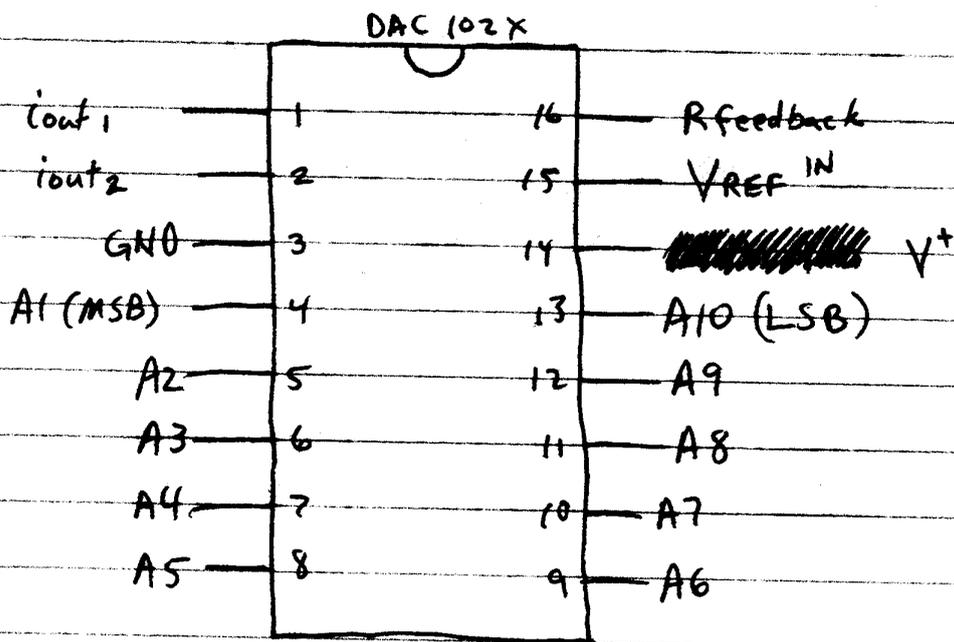
DEVRY INSTITUTE OF
TECHNOLOGY
RABAH AOUI
LABS



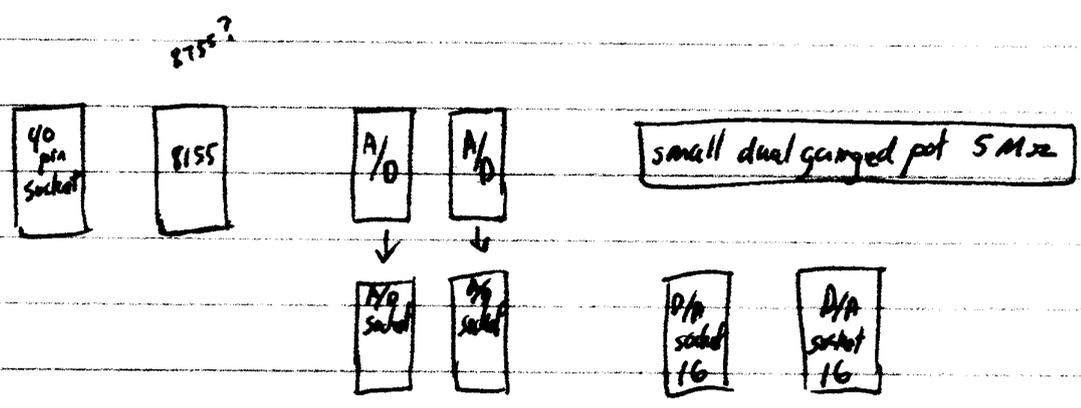
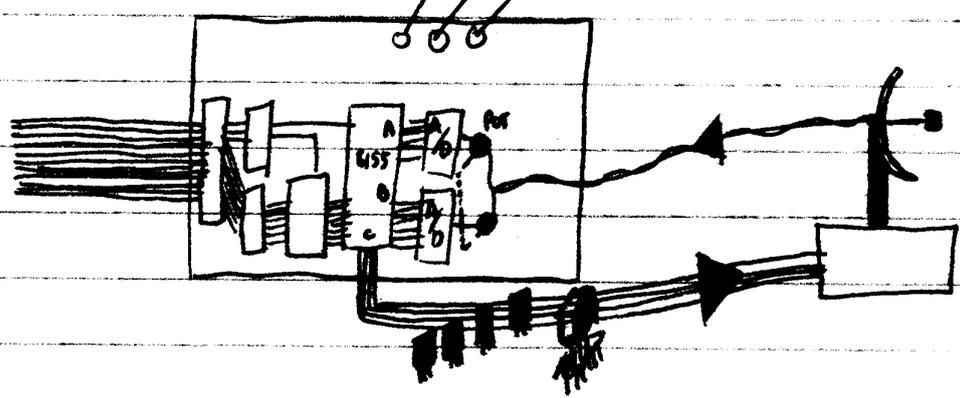
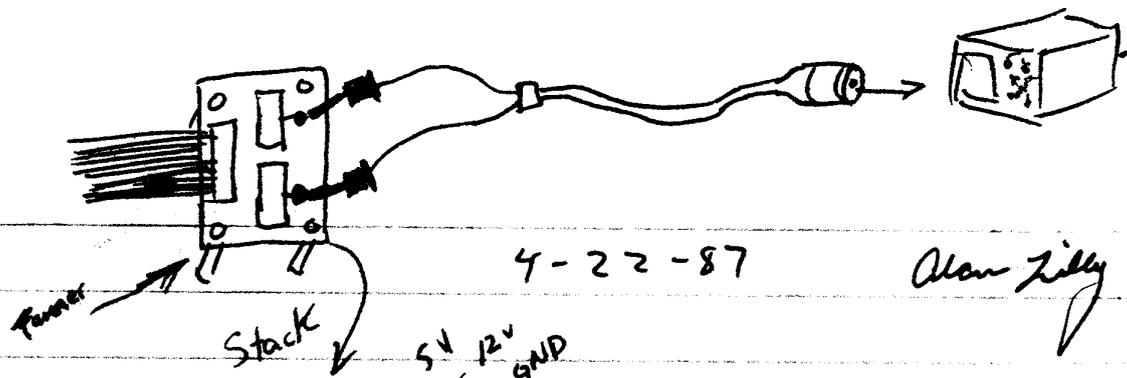
4-22-87

Alan Lilly

Pinout diagram of 1022 D/A



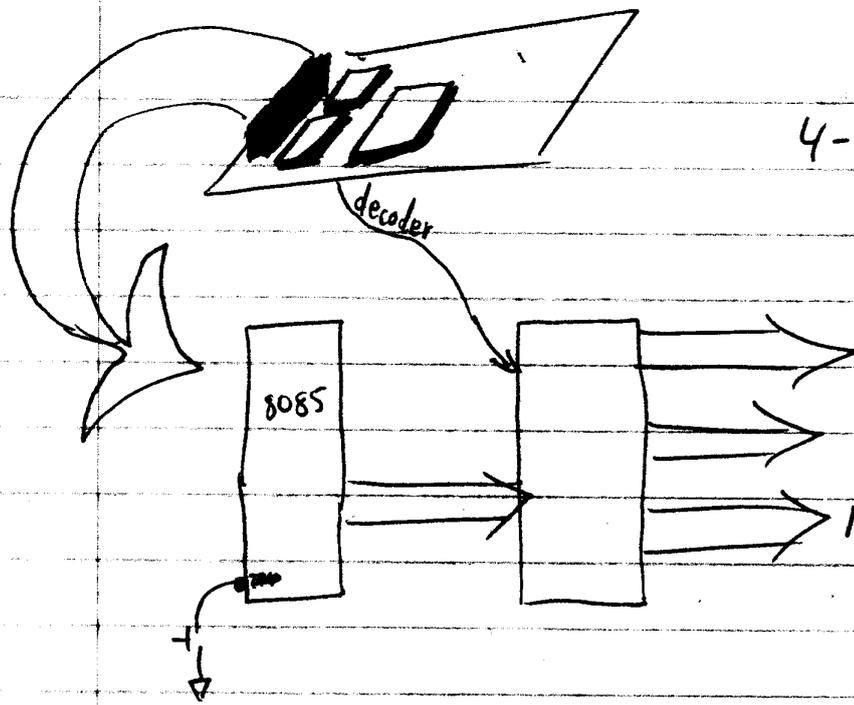
Top View



- * 1 40 pin socket
- * 1 8155
- * 2 A/D (804)
- 2 A/D sockets ← 4 sockets
- 2 16 pin socket ←
- * 1 ganged pot 5MΩ
- 1 identical board + spacers (Radio Shack)

4-22-87

Alan Jilly

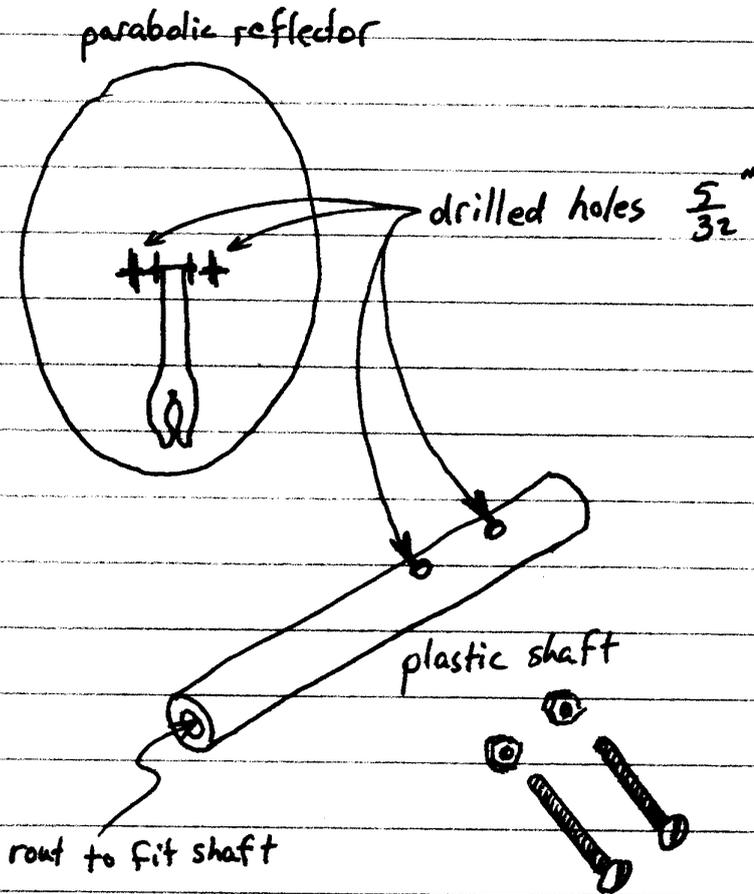


LIBRARY OF
FABRIQUE

Alan Kelly

4-29-87

Mechanical mount for motor shaft and parabolic reflector



Mounted with 2 screws and bolts

Alan Lilly

Test program (motor)

010

MVI A, 0C CW "Port C output PA + PB input"

OUT 18 "30 C/SR for pin 3 I/O 74138"

MVI A, 11

MVI C, 0A "10 steps"

back OUT 1B "PC"

RLC

MVI B, ~~FF~~ 10 ~~Bob~~

Bob MVI D, FF

JOE DCR D

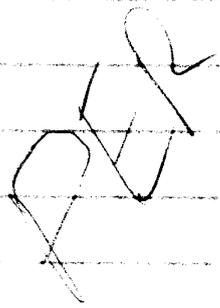
JNZ JOE

DCR B

JNZ Bob

DCR C

JNZ back



1400 - 3E 140E - FF

1401 - 0C 140F - 15

1402 - D3 1410 - C2

1403 - 18 1411 - 0F

1404 - 3E 1412 - 14

1405 - 11 1413 - 05

1406 - 0E 1414 - C2

* rev 1407 - 64 1415 - 0D

1408 - D3 1416 - 14

1409 - 1B 1417 - 0D

RLC 140A - 07 1418 - C2

140B - 06 1419 - 08

+D 140C 07 141A - 14

140D 16 141B - FF

* minimum time

07
FF

11

Alan Jolly

C/R - 18 RAM MEMORY 0800 - 08FF

PA - 19 200 steps = 1 sweep

PB - 1A

PC - 1B

LXI SP, 0880

MVI A, 0C

OUT 18

Scan left MVI B, A0

MVI C, C8

MVI D, 11

back IN 19

CMP B

JNC track

MOV A, D

OUT 1B

RLC

MOV D, A

CALL time

DCR C

JNZ back

Scan Right ~~MVI B, A0~~

MVI C, C8

~~MVI D, 11~~

back2 IN 19

CMP B

JNC track

MOV A, D

OUT 1B

RRC

MOV D, A

CALL time

DCR C

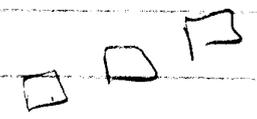
JNZ back2

JMP Scan left

14 13 12 11 10
 Alan Tilly

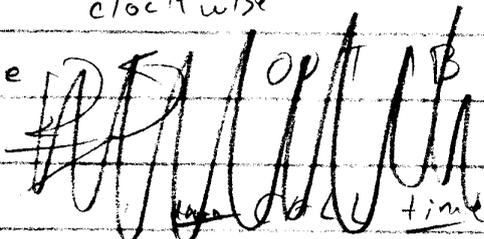
```

track  MVI A, 0C
        OUT 18
        MVI D, 11
start  IN 19  ← ANI 11111000
        MOV E, A
        IN 1A  ← ANI 11111110
        CMP E  ← JZ start
        JC ahead
        MOV A, D
        RLC    ← counter clock
        JMP down
  
```



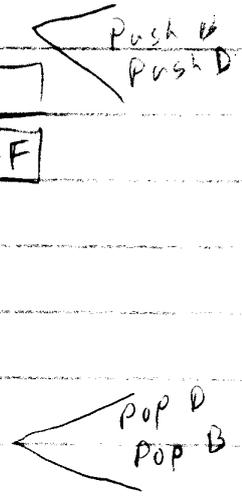
```

ahead  MOV A, D
        RRC    ← clock wise
down   CALL time
        OUT 1B
        MOV D, A
        JMP start
  
```



```

time  Push PSW
        MVI A, [ ]
BOB   MVI B, [FF]
JOE   DCR B
        JNZ JOE
        DCR A
        JNZ BOB
        POP PSW
        RET
  
```



Alan Lilly

Operations testing

- ① retest motor prog.
find max motor speed

DISPLAY TEST PROGRAM

MVI A, 84	1400 - 3E
<u>back</u> CALL display	1401 - 84
JMP back	1402 - CD
	1403 - <u>60</u>
	1404 - <u>05</u>
	1405 - C3
	1406 - <u>02</u>
	1407 - <u>14</u>

A/D CONVERTER TEST PROGRAM

MVI A, 0C		
OUT 18	1400 - 3E	1409 3E - C3
IN 19	1401 - 0C	0A - 10 - 04
<u>back</u> CALL <u>display</u>	1402 - D3	B - 06 - 14
JMP <u>back</u>	1403 - 18	C - FF
	1404 - DB	D - 05
	1405 - 19	E - C2
Adjust ↻ = 00	1406 - CD	F - 0D
	1407 - <u>60</u>	- 14
	1408 - <u>05</u>	- 3D
		- C2
		0B
		14

31		14	
3B / 80	Stack Pointer	55 / 0D	3B
3C / 14		56 / C2	6F / 14
3D / 3E		57 / 48	70 /
3E / 0C		58 / 14	
3F / D3	Port Setup	59 / 0E	
40 / 18		5A / C8	
41 / 06		5B / DB	
42 / E0		5C / 19	
43 / 0E	Register Setup	5D / B8	
44 / C8		5E / D2	
45 / 16		5F / 00	
46 / 11		60 / 14	
47 / DB		61 / 7A	
48 / 19		62 / D3	
49 / B8		63 / 1B	
4A / D2		64 / 0F	
4B / 00		65 / 57	
4C / 14		66 / CD	
4D / 7A		67 / 26	
4E / 03		68 / 14	
4F / 1B		69 / 0D	
50 / 07		6A / C2	
51 / 57		6B / 5C	
52 / CD		6C / 14	
53 / 26		6D / C3	
54 /		6E /	

#TITLE (PROJ)

CHIP #3

;PROJECT PROGRAM BY ALAN LILLY

```
STACK      SEGMENT
STACK      ENDS
DATA       SEGMENT      AT 0000H
DATA       ENDS
CODE       SEGMENT      AT 0FF80H
MAIN       ASSUME       CS:CODE,DS:DATA,SS:STACK
           PROC        FAR
           MOV         AX,DATA
           MOV         DS,AX
           MOV         CH,03H
JOE3:      MOV         DX,0FFFFH
AGAIN3:    DEC         DX
           JNZ        AGAIN3
           DEC         CH
           JNZ        JOE3
           MOV         AX,000DH
           OUT        00H,AL
SAMPLE:    MOV         AH,10H
           MOV         BX,00FFH
BACK:      IN         AL,02H
           MOV         DX,0057H
JOE4:      DEC         DX
           JNZ        JOE4
           MOV         [BX-1],AL
           DEC         BX
           JNZ        BACK
START:     MOV         BX,001CH
           MOV         CL,0DH
UPPER:     MOV         AL,[BX]
           ROR         AL,1
           ROR         AL,1
           ROR         AL,1
           ROR         AL,1
           OUT        03H,AL
           DEC         CL
           MOV         AL,CL
           INC         CL
           ROL         AL,1
           ROL         AL,1
           ROL         AL,1
           ROL         AL,1
           OR          AL,0FH
           OUT        01H,AL
JOE:       MOV         CH,01H
AGAIN:     MOV         DX,0650H
           DEC         DX
           JNZ        AGAIN
           DEC         CH
           JNZ        JOE
           DEC         BX
           DEC         CL
           JNZ        UPPER
LOWER:     MOV         CL,0FH
           MOV         AL,[BX]
           ROR         AL,1
           ROR         AL,1
           ROR         AL,1
           ROR         AL,1
           OUT        03H,AL
           DEC         CL
           MOV         AL,CL
           INC         CL
           OR          AL,0F0H
           OUT        01H,AL
JOE2:     MOV         CH,01H
AGAIN2:    MOV         DX,0650H
           DEC         DX
           JNZ        AGAIN2
           DEC         CH
           JNZ        JOE2
           DEC         BX
           DEC         CL
           JNZ        LOWER
           DEC         AH
           JNZ        START
           JMP        SAMPLE
MAIN       ENDP
CODE       ENDS
BOOT       SEGMENT      AT 0FFFFH
           ASSUME     CS:BOOT
           JMP        FAR PTR MAIN
BOOT       ENDS
END        MAIN
```

LOCATION	OBJECT	CODE	LINE	SOURCE	LINE
			1	"8085"	
			2		
0000	3E0C		3	ORG	0000H
0002	D308		4	MVI	A,0CH
0004	06F0		5	OUT	08H
0006	0EC8		6	SCAN	MVI B,0F0H
0008	1611		7	MVI	C,0C8H
000A	DB09		8	MVI	D,11H
000C	B8		9	BACK	IN 09H
000D	D20045		10	IN	09H
0010	7A		11	CMP	B
0011	D30B		12	JNC	TRACK
0013	07		13	MOV	A,D
0014	57		14	OUT	08H
0015	2610		15	RLC	
0017	2EFF		16	MOV	D,A
0019	2D	BOB1	17	MVI	H,10H
001A	C20019	JOE1	18	MVI	L,0FFH
001D	25		19	DCR	L
001E	C20017		20	JNZ	JOE1
0021	0D		21	DCR	H
0022	C2000A		22	JNZ	BOB1
0025	0EC8		23	DCR	C
0027	DB09		24	JNZ	BACK
0029	B8	AGAIN	25	MVI	C,0C8H
002A	D20045		26	IN	09H
002D	7A		27	CMP	B
002E	D30B		28	JNC	TRACK
0030	0F		29	MOV	A,D
0031	57		30	OUT	08H
0032	2610		31	RRC	
0034	2FFF		32	MOV	D,A
0036	2D	BOB2	33	MVI	H,10H
0037	C20036	JOE2	34	MVI	L,0FFH
003A	25		35	DCR	L
003B	C20034		36	JNZ	JOE2
003E	0D		37	DCR	H
003F	C20027		38	JNZ	BOB2
0042	C30004		39	DCR	C
0045	3E0C		40	JNZ	AGAIN
0047	D308	TRACK	41	JMP	SCAN
0049	1611		42	MVI	A,0CH
004B	DB09		43	OUT	08H
004D	E6F8	START	44	MVI	D,11H
004F	5F		45	IN	09H
0050	DR0A		46	ANI	0F8H
0052	E6F8		47	MOV	E,A
0054	B8		48	IN	0AH
0055	CA004B		49	ANI	0F8H
0058	DA0060		50	CMP	E
005B	7A		51	JZ	START
005C	07		52	JC	AHEAD
005D	C30062		53	MOV	A,D
0060	7A		54	RLC	
0061	0F	AHEAD	55	JMP	DOWN
0062	2610		56	MOV	A,D
0064	2EFF	DOWN	57	RRC	
		BOB3	58	MVI	H,10H
			59	MVI	L,0FFH
0066	2D	JOE3	60	DCR	L
0067	C20066		61	JNZ	JOE3
006A	25		62	DCR	H
006B	C20064		63	JNZ	BOB3
006E	D30B		64	OUT	08H
0070	57		65	MOV	D,A
0071	C3004B		66	JMP	START

TITLE PROGRAM 4 By Alan Lilly

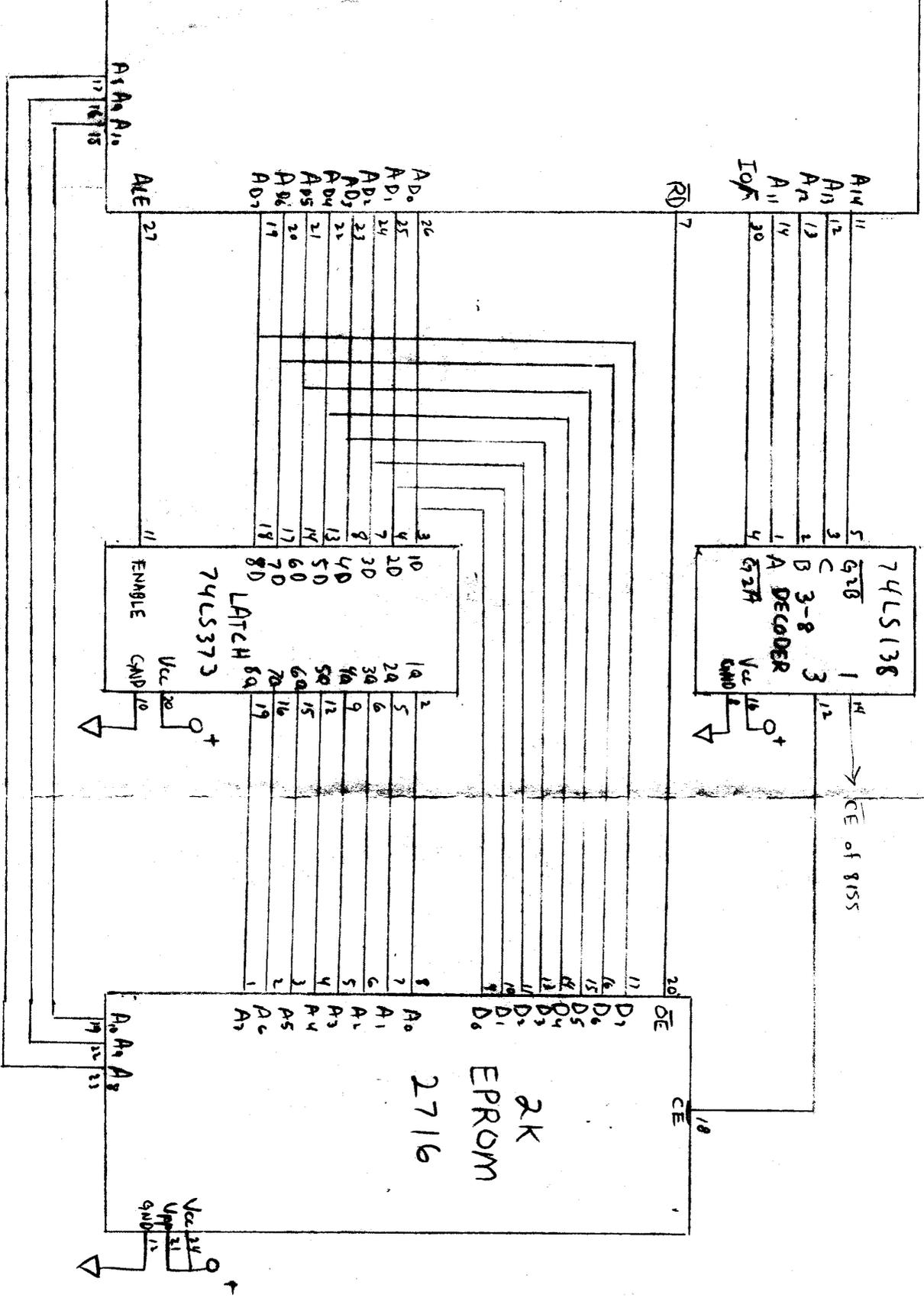
;Description :DISPLAY A ROTATING BITS ON I/O MODULE

;Ports used :
;Procedures used :
;Registers used :

DATA SEGMENT
SEED DB 0
RATE DB 0
PORTA DW 0FFF9H
PORTB DW 0FFFBH
PORTC DW 0FFFDH
CONTROL DW 0FFFFH
DATA ENDS

CODE SEGMENT
ASSUME CS:CODE,DS:DATA
MOV AX,0010H
MOV DS,AX
MOV DX,CONTROL
MOV AL,99H ;SETUP PORTS
OUT DX,AL
START: MOV DX,PORTA
IN AL,DX ;READ SWITCHES
MOV AH,AL
AND AL,0F0H
MOV SEED,AL ;AL HAS SEED
NOT AH
AND AH,0FH
MOV RATE,AH ;AH HAS RATE
INC AH
AGAIN: MOV CL,AH ;PAUSE AT RATE
DELAY: MOV BX,0500H
BACK: DEC BX
JNZ BACK
DEC CL
JNZ DELAY
MOV DX,PORTB
OUT DX,AL ;LIGHT LEDS
ROR AL,1
MOV BL,AL
MOV DX,PORTC
IN AL,DX ;CHECK DEBOUNCE BUTTON
CMP AL,01H
JZ START
MOV AL,BL
JMP AGAIN
CODE ENDS
END

EPROM 2K EXPANSION



AL

Alan Lilly

I/O pin 3
mem enable

1800-FFFF

011
100

SID of SOP
for decoder output

chr

00011000

18

PA 19
PB 1A
PC 1B

